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**UTILITY PATENT APPLICATION  
TRANSMITTAL LETTER  
UNDER 37 C.F.R. 1.53(b)**

ATTORNEY DOCKET NO.:  
2885/31

Address to:  
Assistant Commissioner for Patents  
Washington D.C. 20231  
Box Patent Application

Transmitted herewith for filing is the patent application of

Inventor(s): **Martin VORBACH and Robert MÜNCH**

For : **METHOD OF REPAIRING INTEGRATED CIRCUITS**

Enclosed are:

1. 25 sheets of specification and 4 sheets of claims.
2. 18 sheets of drawings.
3. Information Disclosure Statement
4. Also enclosed:

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5. Related Application:

This application is a continuation of International Patent Application  
PCT/DE98/03682 filed on December 15, 1998.

6. The filing fee is not being paid at this time.

Dated: *21 June 2000*

By:



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Method of repairing integrated circuits